

High-voltage optocouplers

CNY17G/CNY17GF

FEATURES

- High current transfer ratio and a low saturation voltage, making the devices suitable for use with TTL integrated circuits
- High degree of AC and DC insulation (3750 V (RMS) and 5300 V (DC))
- Working voltage of 2.5 kV (DC)
- Fast switching
- A pin distance of 10.16 mm.

DESCRIPTION

The CNY17G and CNY17GF are optocouplers consisting of an infrared emitting GaAs diode and a silicon npn phototransistor, in a dual-in-line (DIL) SOT231 plastic envelope. The base of the phototransistor is connected for CNY17G and unconnected for CNY17GF.

PINNING - CNY17G

PIN	DESCRIPTION
1	anode
2	cathode
3	not connected
4	emitter
5	collector
6	base

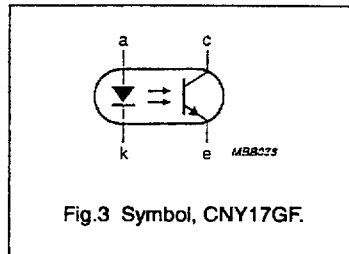
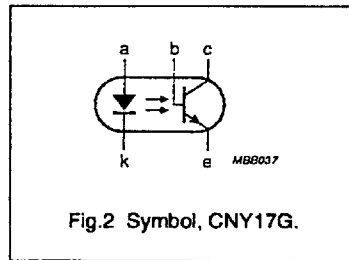
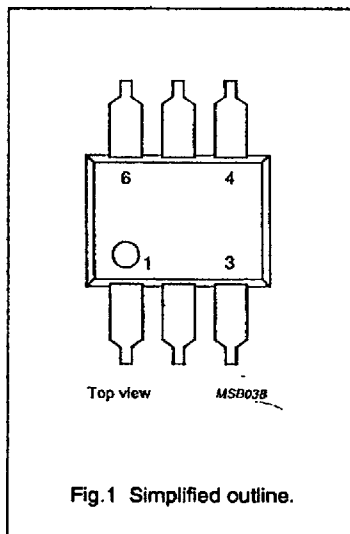
PINNING - CNY17GF

PIN	DESCRIPTION
1	anode
2	cathode
3	not connected
4	emitter
5	collector
6	not connected



APPROVALS

STANDARD	REFERENCE
UL	covered under UL component recognition FILE E90700
BSI	certification in accordance with BS415:1990; BS7002:1989, class II applications
NORDIC	tested for applications (reinforced isolation); Class II applications for pluggable apparatus in normal tight execution
SETI	in accordance with IEC 65, 380, 950 & 335
SEMKO	in accordance with IEC 65, 380, 950 & 335
NEMKO	in accordance with IEC 65, 380, 950 & 335
DEMKO	in accordance with IEC 65, 380, 950 & 335
VDE	approved in accordance with VDE 0883/6.80 reference voltage (VDE 0110b Tab 4): 500 V (AC)/600 V (DC) complied for reinforced isolation at 250 V (AC) with: DIN IEC 380/VDE 0806/8.81 DIN IEC 435/VDE0805 "ENTWURF", Nov. 84 DIN 57804/VDE 0804/1.83 (isolation group C) DIN VDE 0860/8.86/HD 195 S4
CECC	capability of approval: GaAs optocouplers with phototransistor output



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QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Diode					
V_R	reverse voltage	DC value	–	6	V
I_F	forward current	DC value	–	100	mA
P_{tot}	total power dissipation	up to $T_{amb} = 25\text{ °C}$	–	200	mW
Transistor					
I_C	collector current	DC value	–	100	mA
V_{CE0}	collector-emitter voltage	open base	–	70	V
P_{tot}	total power dissipation	up to $T_{amb} = 25\text{ °C}$	–	200	mW
Optocoupler					
I_C/I_F	DC current transfer ratio (CTR)	$V_{CE} = 5\text{ V};$ $I_F = 10\text{ mA}$			
	CNY17G-1/GF-1		0.40	0.80	
	CNY17G-2/GF-2		0.63	1.25	
	CNY17G-3/GF-3		1.00	2.00	
	CNY17G-4/GF-4		1.60	3.20	
V_{io}	isolation voltage	DC value	5.3	–	kV
		AC (RMS value)	3.75	–	kV
Switching times					
t_{on}	turn on time	$I_C = 2\text{ mA};$ $V_{CE} = 10\text{ V};$ $R_L = 100\text{ }\Omega$	–	10	μs
t_{off}	turn off time	$I_C = 2\text{ mA};$ $V_{CE} = 10\text{ V};$ $R_L = 100\text{ }\Omega$	–	10	μs

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LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Diode					
V_R	reverse voltage		-	6	V
I_F	forward current	DC value	-	100	mA
I_{FRM}	forward current	peak value; $t_{on} = 10 \mu\text{s}$; $\delta = 0.01$	-	3	A
P_{tot}	total power dissipation	up to $T_{amb} = 25^\circ\text{C}$	-	200	mW
Transistor					
V_{CEO}	collector-emitter voltage	open base	-	70	V
V_{CBO}	collector-base voltage (CNY17G only)	open emitter	-	70	V
V_{EBO}	emitter-base voltage (CNY17G only)	open collector	-	7	V
I_C	collector current	DC value	-	100	mA
P_{tot}	total power dissipation	up to $T_{amb} = 25^\circ\text{C}$	-	200	mW
Optocoupler					
T_{stg}	storage temperature range		-55	150	$^\circ\text{C}$
T_{amb}	ambient operating temperature range		-40	100	$^\circ\text{C}$
T_j	junction temperature		-	125	$^\circ\text{C}$
T_{sld}	soldering temperature up to the seating plane	$T_{sld} < 10 \text{ s}$	-	260	$^\circ\text{C}$

THERMAL RESISTANCE

SYMBOL	PARAMETER	MAX.	UNIT
Diode			
$R_{th(j-a)}$	from junction to ambient in free air	500	K/W
$R_{th(j-a)}$	from junction to ambient when mounted on PCB	400	K/W
Transistor			
$R_{th(j-a)}$	from junction to ambient in free air	500	K/W
$R_{th(j-a)}$	from junction to ambient when mounted on PCB	400	K/W

ISOLATION RELATED VALUES

SYMBOL	PARAMETER	CONDITIONS	MIN.	UNIT
L(I01)	external air gap (clearance)	between input and output terminals	9.6	mm
L(I02)	external tracking path (creepage distance)	between input and output terminals	8	mm
	internal plastic gap (clearance)	isolation thickness between emitter and receiver	1	mm

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CLASSIFICATION CATEGORIES

Tracking resistance	KB-100/A
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CHARACTERISTICS

$T_j = 25\text{ }^\circ\text{C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Diode						
V_F	forward voltage	$I_F = 10\text{ mA}$	–	1.1	1.5	V
I_R	reverse current	$V_R = 6\text{ V}$	–	–	10	μA
Transistor						
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 10\text{ mA}$	70	–	–	V
$V_{(BR)CBO}$	collector-base breakdown voltage (CNY17G only)	$I_C = 0.1\text{ mA}$	70	–	–	V
$V_{(BR)EBO}$	emitter-base breakdown voltage (CNY17G only)	$I_E = 0.1\text{ mA}$	7	–	–	V
I_{CEO}	collector-emitter cut-off current (dark)	$I_F = 0$; $V_{CE} = 10\text{ V}$	–	2	50	nA
Optocoupler						
I_C/I_F	DC current transfer ratio (CTR)	$I_F = 10\text{ mA}$; $V_{CE} = 5\text{ V}$				
			CNY17G-1/GF-1	0.4	–	0.8
			CNY17G-2/GF-2	0.63	–	1.25
			CNY17G-3/GF-3	1	–	2
	CNY17G-4/GF-4	1.6	–	3.2		
	CNY17G-1/GF-1 CNY17G-2/GF-2 CNY17G-3/GF-3 CNY17G-4/GF-4	$I_F = 1\text{ mA}$; $V_{CE} = 5\text{ V}$				
				0.13	–	–
				0.22	–	–
			0.34	–	–	
	0.56	–	–			
$V_{CE sat}$	collector-emitter saturation voltage	$I_F = 10\text{ mA}$; $I_C = 2.5\text{ mA}$	–	–	0.3	V
I_{CEW}	collector cut-off current (dark) (see Fig.4)	$V_W = 2.5\text{ kV (DC)}$; $V_{CC} = 10\text{ V}$; $T_{amb} = 25\text{ }^\circ\text{C}$; notes 1 and 2	–	–	200	nA
		$V_W = 2.5\text{ kV (DC)}$; $V_{CC} = 10\text{ V}$; $T_{amb} = 70\text{ }^\circ\text{C}$; notes 1 and 2	–	–	2	μA

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SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Diode						
V_{IO}	isolation voltage (note 3)	DC value; $t = 1 \text{ min}$	5.3	–	–	kV
		RMS value; $t = 1 \text{ min}$	3.75	–	–	kV
C_{IO}	capacitance between input and output	$V_{IO} = 0$; $f = 1 \text{ MHz}$	–	0.4	1	pF
R_{IO}	insulation resistance between input and output	$V_{IO} = \pm 500 \text{ V}$	10^{12}	10^{13}	–	Ω
Switching times (see Figs 5 and 6)						
t_{on}	turn-on time	$I_C = 2 \text{ mA}$; $V_{CC} = 10 \text{ V}$; $R_L = 100 \Omega$	–	5	10	μs
t_{off}	turn-off time	$I_C = 2 \text{ mA}$; $V_{CC} = 10 \text{ V}$; $R_L = 100 \Omega$	–	5	10	μs

Notes

1. This parameter is the maximum collector-emitter leakage current measured when a high voltage is applied between the shorted diode leads and the transistor emitter.
2. For quality assurance, the two parameters are tested on a sample basis for 1000 hrs.
3. Every product is tested by applying an isolation test voltage of 4500 V (RMS) for 2 s between the shorted input (diode) leads and the shorted output (phototransistor) leads with a detection current of approximately 1 μA .

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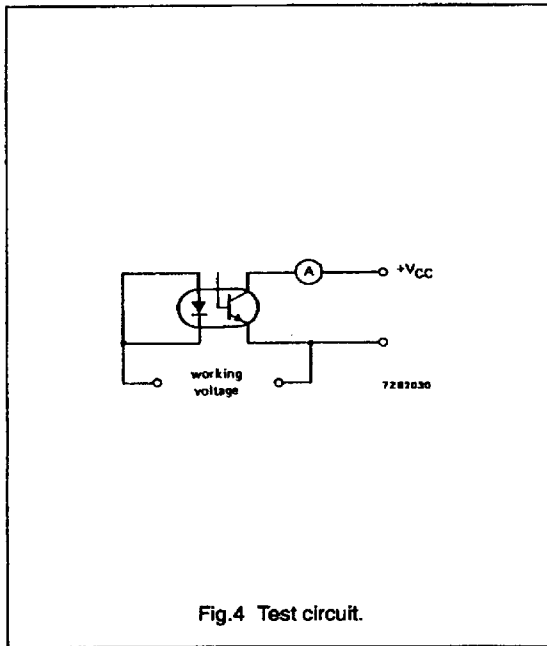


Fig.4 Test circuit.

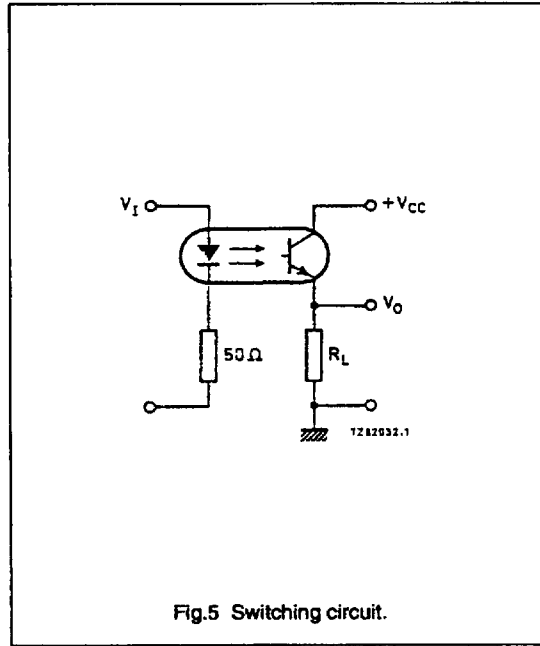


Fig.5 Switching circuit.

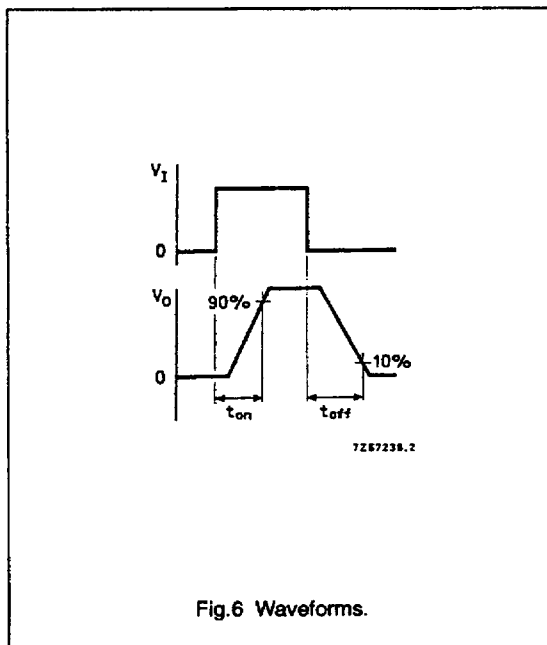
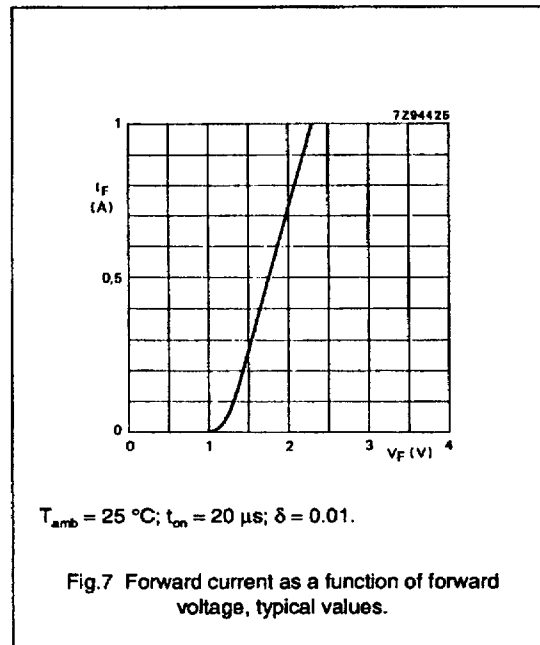


Fig.6 Waveforms.

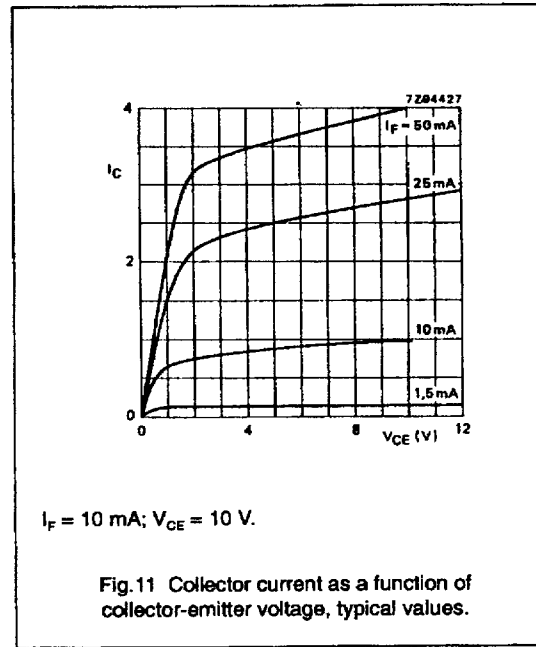
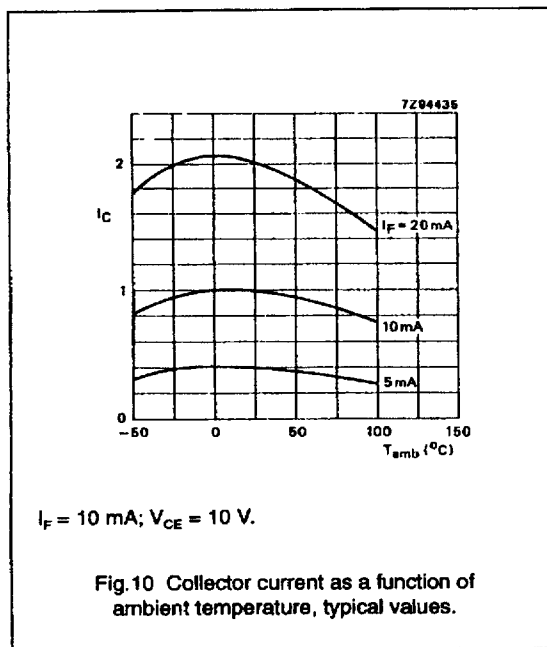
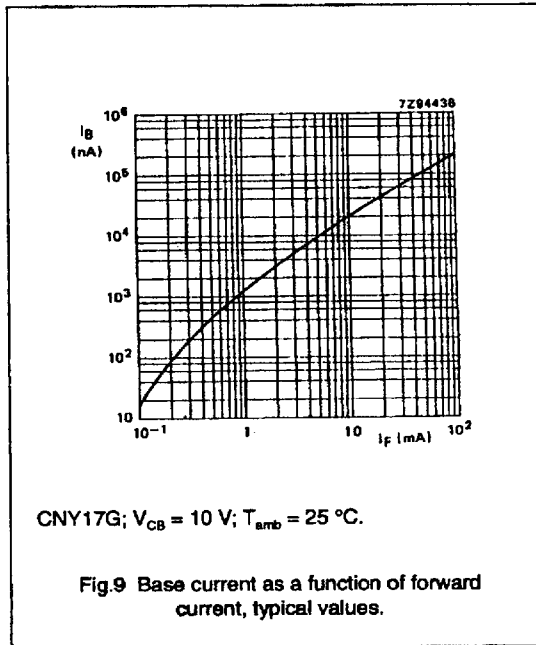
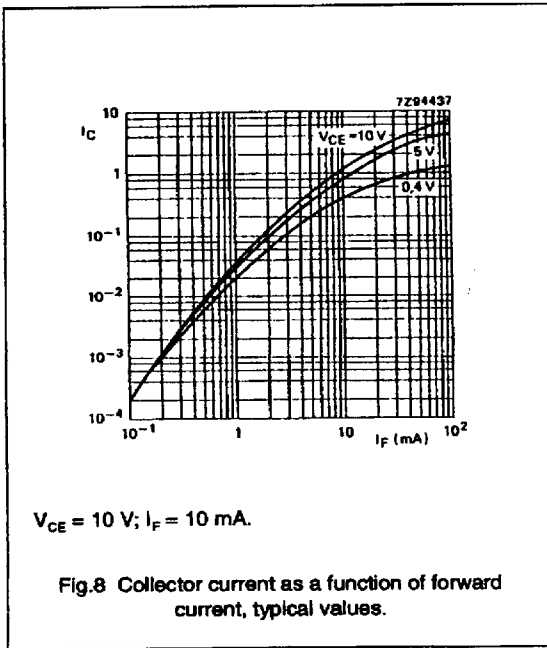


$T_{amb} = 25\text{ }^{\circ}\text{C}; t_{on} = 20\text{ }\mu\text{s}; \delta = 0.01.$

Fig.7 Forward current as a function of forward voltage, typical values.

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